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S/N 10/679,593 PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Deshanand Singh et al.	Examiner: Tat, Binh C
Serial No.:	10/679,593	Group Art Unit: 2825
Filed:	October 6, 2003	Docket No.: ALT.P024 (A01002)
Title:	Method and Apparatus for Performing Layout-Driven Optimizations on Field Programmable Gate Arrays	

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Dear Sir,

This Amendment is submitted with a Request for Continued Examination. A Notice of Appeal was filed with the U.S. Patent and Trademark Office on December 12, 2007. Submitted herewith is a credit card payment form authorizing the payment of $120 in payment for the petition fee for a 1 month extension of time. Please charge any deficiencies in those fees, or credit any overpayment to Deposit Account 50-1624.

PAGE 6/19 * RCVD AT 3/10/2008 9:58:54 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-5/14 * DNIS:2738300 * CSID:2173567373 * DURATION (mm-ss):03-34